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Parallel pseudo-random generator for emulating a serial pseudo-random generator and method for carrying out same.

A parallel pseudo-random generator for emulating a serial pseudo-random generator that generates serial outputs such that the next serial output value is based upon an Exclusive OR combination of at least two preceding serial output values the maximum preceding serial output value defined as the Pth preceding serial output value, where P is an integer greater than one; comprising:

A) at least P latches, each latch having an output having a logic value 1 or 0 and an input operable upon receipt of a clock signal, for receipt of data for controlling the next logic value on the latch output;

B) at least P Exclusive OR gates, each having at least two inputs and one output, each Exclusive OR gate output connected to a corresponding input of one latch so as to define the next value of the latch output upon receipt of the next clock signal; and

C) means for connecting each input of each Exclusive OR gate to one latch output so that the output of each Exclusive OR gate represents the

corresponding next value of the latch to which This Exclusive Or gate output is connected.

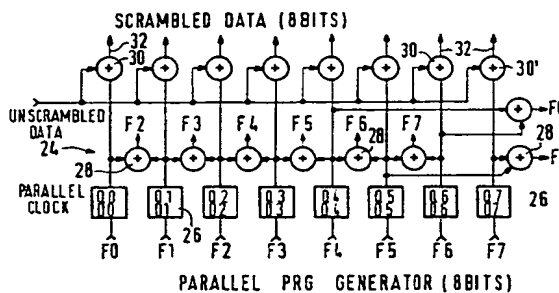
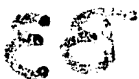


Fig. 2



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EUROPEAN SEARCH REPORT

Application Number

EP 90 10 8555

DOCUMENTS CONSIDERED TO BE RELEVANT

Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl.5)
X	ELEKTRONIK, vol. 32, no. 26, December 1983, pages 67-70; T. HERMES et al.: "Parallel arbeitende Scrambler, Descrambler und Zufallsfolgen-Generatoren" * Page 68, paragraph 3; page 69, paragraph 4; figure 5 *	1-4	H 04 L 25/49
T	THE RADIO AND ELECTRONIC ENGINEER, vol. 45, no. 4, April 1975, pages 171-176; J.J. O'REILLY: "Series-parallel generation of m-sequences" * Whole document *	1-4	
E	US-A-4 965 881 (DILLEY) * Whole document *	1-4	
			TECHNICAL FIELDS SEARCHED (Int. Cl.5)
			H 04 L H 03 K
The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 17-03-1992	Examiner GRIES T.M.
CATEGORY OF CITED DOCUMENTS			
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